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PATENT

Attorney Docket No. 10113671



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Meng-Hung CHEN**

Filed: **1/21/2004**

Appl. No.: **10/761,702**

Examiner: **Warren, Matthew E**

Conf. No.: **3361**

Art Unit: **2815**

Title: **BIT LINE CONTACT STRUCTURE AND FABRICATION METHOD
THEREOF**

Date: **March 8, 2005**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §1.56, the materials listed on the attached form PTO-1449 are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application.

The information disclosure statement submitted herewith is being filed within three months of the filing date of the above-identified application or date of entry into the national stage of an international application related thereto, or, to the best of applicant's knowledge, before the mailing date of the first Office Action on the merits, whichever occurs last.

Should the Office mail an Office Action on the merits before or on the same day as the mailing

of this IDS, Applicant furthermore certifies under 37 C.F.R. 1.97(e) that each item of information contained in this statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. A copy of the foreign office action dated January 13, 2005 is enclosed. . In addition, a copy of each non-U.S. patent or patent application publication is enclosed.

The filing of this information disclosure statement shall not be construed to be a representation that a search has been conducted, nor shall it be construed as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

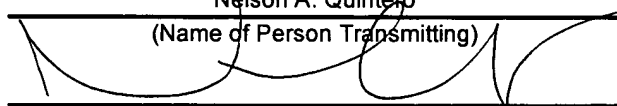
It is respectfully requested that the Examiner return a copy of the attached form PTO-1449 with initials or other appropriate marks indicating consideration of the cited materials.

Please charge any required additional fee or underpayment of fee to deposit account No.


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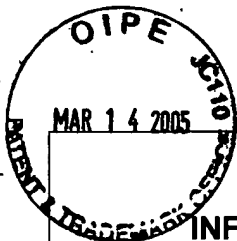
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(Signature)
March 8, 2005
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Respectfully submitted,


Nelson A. Quintero
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

| | | | | | |
|-------|---|----|---|------------------------|-------------------|
| Sheet | 1 | of | 1 | Application Number | 10761,702 |
| | | | | Filing Date | January 21, 2004 |
| | | | | First Named Inventor | Meng-Hung CHEN |
| | | | | Art Unit | 2815 |
| | | | | Examiner Name | Warren, Matthew E |
| | | | | Attorney Docket Number | 10113671 |

U.S. PATENT DOCUMENTS

| Examiner Initials | Cite No. | Document Number (Number – kind code) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines Where Passages or Relevant Figures Appear |
|-------------------|----------|--------------------------------------|-----------------------------|---|---|
| | A1 | US-6,006,569 | 05-23-2000 | | |
| | A2 | | | | |
| | A3 | | | | |
| | A4 | | | | |
| | A5 | | | | |
| | A6 | | | | |
| | A7 | | | | |

FOREIGN PATENT DOCUMENTS

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|-------------------|----------|--------------------------------------|-----------------------------|---|---|---|
| | B1 | | | | | |
| | B2 | | | | | |

OTHER ART

| Examiner Initials | Cite No. | Include name of author (in CAPS), title of article (when appropriate), title of the item (book, magazine, symposium, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T |
|-------------------|----------|---|---|
| | C1 | HONG XIAO, "Introduction to semiconductor manufacturing technology", 2001, pages 568-569, Prentice Hall Inc., USA | |
| | C2 | STANLEY WOLF, "Silicon Processing for the VLSI Era: Process Integration, Vol. 2", 1990, pages 214-217, Lattice Press, USA | |
| | C3 | | |
| | C4 | | |
| | C5 | | |

Form PTO-1449 (modified)

(v08/25/2002)

P109580NAQ

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